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utilized in devices with smaller arrays, but multi-megapixel chips will require pixel sizes in the 3 to 4 micron range. In order to achieve these dimensions, CMOS image sensors must be produced on 0.25-micron or narrower fabrication lines. Introduction to CMOS Image Sensors - Microscope Pixel Design and Evaluation in CMOS Image Sensor Technology S. Vargas-Sierra 1, E. Roca, G. Liñán-Cembrano Instituto de Microelectrónica de Sevilla (IMSE), Centro Nacional de Microelectrónica (CNM), Pixel Design and Evaluation in CMOS Image Sensor Technology In

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