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INTR is a signal which if
enabled then
microprocessor has

interrupt enabled it
receives high INR signal &
activates INTA signal, so
another request can't be
accepted till CPU is busy
in servicing interrupt.
Hence (A) is correct
option. Question.
12 Computer Organization
and Architecture 1.1
Computer architecture.
refers to those attributes
of a system visible to a
programmer or, put
another way, those

attributes that have a direct impact on the logical execution of a program. Computer organization refers to the operational units and their interconnections that realize the architectural specifications. SOLUTIONS MANUAL 1.1 Computer architecture refers to those attributes of a system visible to a programmer or, put another way, those attributes that have a direct impact on the logical execution of a program. Computer organization refers to the

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INTR is a signal which if enabled then microprocessor has interrupt enabled it receives high INR signal &

activates INTA signal, so another request can't be accepted till CPU is busy in servicing interrupt. Hence (A) is correct option. Question. 12

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