
Linear Cmos Rf Power Amplifiers For Wireless Applications Efficiency Enhancement And Frequency Tunable Capability Analog Circuits And Signal Processing

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CMOS RF Power Amplifier Design Approaches for Wireless Communications
Linear CMOS RF Power Amplifiers for Wireless Applications
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2018
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ZAVIER JANIYAH

High-efficiency Linear RF Power Amplifiers Development CRC Press
Switchmode RF and Microwave Power Amplifiers, Third Edition is an essential reference book on developing RF and microwave switchmode power amplifiers. The book combines theoretical discussions with practical examples, allowing readers to design high-efficiency RF and microwave power amplifiers on different types of bipolar and field-effect transistors, design any type of high-efficiency switchmode power amplifiers operating in Class D or E at lower frequencies and in Class E or F and their subclasses at microwave frequencies with specified output power, also providing techniques on how to design multiband and broadband Doherty amplifiers using different bandwidth extension techniques and implementation technologies. This book provides the necessary information to understand the theory and practical implementation of load-network design techniques based on lumped and transmission-line elements. It brings a

unique focus on switchmode RF and microwave power amplifiers that are widely used in cellular/wireless, satellite and radar communication systems which offer major power consumption savings. Provides a complete history of high-efficiency Class E and Class F techniques Presents a new chapter on Class E with shunt capacitance and shunt filter to simplify the design of high-efficiency power amplifier with broader frequency bandwidths Covers different Doherty architectures, including integrated and monolithic implementations, which are and will be, used in modern communication systems to save power consumption and to reduce size and costs Includes extended coverage of multiband and broadband Doherty amplifiers with different frequency ranges and output powers using different bandwidth extension techniques Balances theory with practical implementation, avoiding a cookbook approach and enabling engineers to develop better designs, including hybrid, integrated and monolithic implementations

Design and Analysis Techniques for Frequencies from Audio to RF John Wiley & Sons

This book presents the challenges and solutions of designing power amplifiers at RF and mm-Wave frequencies in a silicon-based process technology. It

covers practical power amplifier design methodologies, energy- and spectrum-efficient power amplifier design examples in the RF frequency for cellular and wireless connectivity applications, and power amplifier and power generation designs for enabling new communication and sensing applications in the mm-Wave and THz frequencies. With this book you will learn: Power amplifier design fundamentals and methodologies Latest advances in silicon-based RF power amplifier architectures and designs and their integration in wireless communication systems State-of-the-art mm-Wave/THz power amplifier and power generation circuits and systems in silicon Extensive coverage from fundamentals to advanced design topics, focusing on various layers of abstraction: from device modeling and circuit design strategy to advanced digital and mixed-signal architectures for highly efficient and linear power amplifiers New architectures for power amplifiers in the cellular and wireless connectivity covering detailed design methodologies and state-of-the-art performances Detailed design techniques, trade-off analysis and design examples for efficiency enhancement at power back-off and linear amplification for spectrally-efficient non-constant envelope modulations Extensive coverage of mm-Wave power-generation techniques from the early days of the 60 GHz research to current state-of-the-art reconfigurable, digital mm-Wave PA architectures Detailed analysis of power generation challenges in the higher mm-Wave and THz frequencies and novel technical solutions for a wide range for potential applications, including ultrafast wireless communication to sensing, imaging and spectroscopy Contributions from the

world-class experts from both academia and industry

CMOS RF Power Amplifier Design Approaches for Wireless

Communications John Wiley & Sons

This book provides an overview of current efficiency enhancement and linearization techniques for silicon power amplifier designs. It examines the latest state of the art technologies and design techniques to address challenges for RF cellular mobile, base stations, and RF and mmW WLAN applications. Coverage includes material on current silicon (CMOS, SiGe) RF and mmW power amplifier designs, focusing on advantages and disadvantages compared with traditional GaAs implementations. With this book you will learn: The principles of linearization and efficiency improvement techniques The architectures allowing the optimum design of multimode Si RF and mmW power amplifiers How to make designs more efficient by employing new design techniques such as linearization and efficiency improvement Layout considerations Examples of schematic, layout, simulation and measurement results Addresses the problems of high power generation, faithful construction of non-constant envelope constellations, and efficient and well control power radiation from integrated silicon chips Demonstrates how silicon technology can solve problems and trade-offs of power amplifier design, including price, size, complexity and efficiency Written and edited by the top contributors to the field

Linear CMOS RF Power Amplifiers for Wireless Applications Springer Science & Business Media

The book presents selected, extended and peer reviewed papers from the International Multiconference on System,

Automation and Control held Leipzig in 2016. These are complemented with solicited contributions by international experts. Main topics are: mechanical and thermal sensors, nano sensors, optical, chemical and biomedical sensors. They are applied in energy harvesting, biomedical and environmental measurements and more.

Advanced Design Techniques for RF Power Amplifiers Springer Science & Business Media

Highly Linear Integrated Wideband Amplifiers: Design and Analysis Techniques for Frequencies from Audio to RF deals with the complicated issues involved in the design of high-linearity integrated wideband amplifiers for different operating frequencies. The book demonstrates these principles using a number of high-performance designs. New topologies for high linearity are presented, as well as a novel method for estimating the intermodulation distortion of a wideband signal. One of the most exciting results presented is an enhanced feedback configuration called feedback boosting that is capable of very low distortion. Also important is a statistical method for relating the intermodulation distortion of a wideband signal to the total harmonic distortion (THD) of a single tone. The THD, as opposed to the intermodulation distortion of the wideband signal, is easy to measure and use as a design parameter. Three different applications where high linearity is needed are identified, namely audio power amplifiers, wideband IF amplifiers and RF power amplifiers. For these applications high-performance integrated amplifier designs using novel topologies are presented together with measurement results. The audio amplifiers are built in CMOS and are capable of driving

80Ω loudspeaker loads directly without using any external components. One of the designs can operate on a supply voltage down to 1.5V. Both bipolar and CMOS wideband IF amplifiers are built; they are fully differential and have linearity from DC to 20 MHz.

Finally, an RF power amplifier is built in CMOS, without using inductors, in order to investigate what performance can be achieved without them. Highly Linear Integrated Wideband Amplifiers: Design and Analysis Techniques for Frequencies from Audio to RF is an excellent reference for researchers and designers of integrated amplifiers, and may be used as a text for advanced courses on the topic.

Wireless Technologies Springer Science & Business Media

Analog Circuit Design is based on the yearly Advances in Analog Circuit Design workshop. The aim of the workshop is to bring together designers of advanced analogue and RF circuits for the purpose of studying and discussing new possibilities and future developments in this field. Selected topics for AACD 2007 were: (1) Sensors, Actuators and Power Drivers for the Automotive and Industrial Environment; (2) Integrated PA's from Wireline to RF; (3) Very High Frequency Front Ends.

RF Circuit Design Springer Science & Business Media

Advanced Design Techniques for RF Power Amplifiers provides a deep analysis of theoretical aspects, modelling, and design strategies of RF high-efficiency power amplifiers. The book can be used as a guide by scientists and engineers dealing with the subject and as a text book for graduate and postgraduate students. Although primarily intended for skilled readers, it provides an excellent quick start for

beginners.

Wireless Transceiver Design KIT
Scientific Publishing

This is a rigorous tutorial on radio frequency and microwave power amplifier design, teaching the circuit design techniques that form the microelectronic backbones of modern wireless communications systems. Suitable for self-study, corporate training, or Senior/Graduate classroom use, the book combines analytical calculations and computer-aided design techniques to arm electronic engineers with every possible method to improve their designs and shorten their design time cycles.

CMOS 60-GHz and E-band Power Amplifiers and Transmitters CRC Press
Summarizes the schemes and technologies in RF circuit design, describes the basic parameters of an RF system and the fundamentals of RF system design, and presents an introduction of the individual RF circuit block design. Forming the backbone of today's mobile and satellite communications networks, radio frequency (RF) components and circuits are incorporated into everything that transmits or receives a radio wave, such as mobile phones, radio, WiFi, and walkie talkies. *RF Circuit Design, Second Edition* immerses practicing and aspiring industry professionals in the complex world of RF design. Completely restructured and reorganized with new content, end-of-chapter exercises, illustrations, and an appendix, the book presents integral information in three complete sections: Part One explains the different methodologies between RF and digital circuit design and covers voltage and power transportation, impedance matching in narrow-band case and wide-band case, gain of a raw device,

measurement, and grounding. It also goes over equipotentiality and current coupling on ground surface, as well as layout and packaging, manufacturability of product design, and radio frequency integrated circuit (RFIC). Part Two includes content on the main parameters and system analysis in RF circuit design, the fundamentals of differential pair and common-mode rejection ratio (CMRR), Balun, and system-on-a-chip (SOC). Part Three covers low-noise amplifier (LNA), power amplifier (PA), voltage-controlled oscillator (VCO), mixers, and tunable filters. *RF Circuit Design, Second Edition* is an ideal book for engineers and managers who work in RF circuit design and for courses in electrical or electronic engineering.

From Fundamentals to Advanced Design Methods Springer

This book discusses design techniques, layout details and measurements of several key analog building blocks that currently limit the performance of 5G and E-Band transceivers implemented in deep-scaled CMOS. The authors present recent developments in low-noise quadrature VCOs and tunable inductor-less frequency dividers. Moreover, the design of low-loss broadband transformer-based filters that realize inter-stage matching, power division/combining and impedance transformation is discussed in great detail. The design and measurements of a low-noise amplifier, a downconverter and a highly-linear power amplifier that leverage the proposed techniques are shown. All the prototypes were realized in advanced nanometer scaled CMOS technologies without RF thick to metal option.

IoT and Low-Power Wireless Springer
Science & Business Media
The Power Amplifier (PA) is the last

Radio Frequency (RF) building block in a transmitter, directly driving an antenna. The low power RF input signal of the PA is amplified to a significant power RF output signal by converting DC power into RF power. Since the PA consumes a majority of the power, efficiency plays one of the most important roles in a PA design. Designing an efficient, fully integrated RF PA that can operate at low supply voltage (1.2V), low power, and low RF frequency (433MHz) is a major challenge. The class E Power Amplifier, which is one type of switch mode PA, is preferred in such a scenario because of its higher theoretical efficiency compared to linear power amplifiers. A controllable class E RF power amplifier design implemented in 0.13 microm CMOS process is presented. The circuit was designed, simulated, laid out, fabricated, and tested. The PA will be integrated as a part of a complete wireless transceiver system using the same process

Sensors, Actuators and Power Drivers; Integrated Power Amplifiers from Wireline to RF; Very High Frequency Front Ends Springer Science & Business Media

Low Power Consumption is one of the critical issues in the performance of small battery-powered handheld devices. Mobile terminals feature an ever increasing number of wireless communication alternatives including GPS, Bluetooth, GSM, 3G, WiFi or DVB-H. Considering that the total power available for each terminal is limited by the relatively slow increase in battery performance expected in the near future, the need for efficient circuits is now critical. This book presents the basic techniques available to design low power RF CMOS analogue circuits. It gives circuit designers a complete guide of

alternatives to optimize power consumption and explains the application of these rules in the most common RF building blocks: LNA, mixers and PLLs. It is set out using practical examples and offers a unique perspective as it targets designers working within the standard CMOS process and all the limitations inherent in these technologies.

[Distributed Transformers for Broadband Monolithic Millimeter-Wave Integrated Power Amplifiers](#) Springer Science & Business Media

Doherty Power Amplifiers: From Fundamentals to Advanced Design Methods is a great resource for both RF and microwave engineers and graduate students who want to understand and implement the technology into future base station and mobile handset systems. The book introduces the very basic operational principles of the Doherty Amplifier and its non-ideal behaviors. The different transconductance requirements for carrier and peaking amplifiers, reactive element effect, and knee voltage effect are described. In addition, several methods to correct imperfections are introduced, such as uneven input drive, gate bias adaptation, dual input drive and the offset line technique. Advanced design methods of Doherty Amplifiers are also explained, including multistage/multiway Doherty power amplifiers which can enhance the efficiency of the amplification of a highly-modulated signal. Other covered topics include signal tracking operation which increases the dynamic range, highly efficient saturated amplifiers, and broadband amplifiers, amongst other comprehensive, related topics. Specifically written on the Doherty Power Amplifier by the world's leading expert,

providing an in-depth presentation of principles and design techniques Includes detailed analysis on correcting non-ideal behaviors of Doherty Power Amplifiers Presents advanced Doherty Power Amplifier architectures

RF CMOS Power Amplifiers: Theory, Design and Implementation John Wiley & Sons

Advanced concepts for wireless technologies present a vision of technology that is embedded in our surroundings and practically invisible. From established radio techniques like GSM, 802.11 or Bluetooth to more emerging technologies, such as Ultra Wide Band and smart dust motes, a common denominator for future progress is the underlying integrated circuit technology. Wireless Technologies responds to the explosive growth of standard cellular radios and radically different wireless applications by presenting new architectural and circuit solutions engineers can use to solve modern design problems. This reference addresses state-of-the art CMOS design in the context of emerging wireless applications, including 3G/4G cellular telephony, wireless sensor networks, and wireless medical application. Written by top international experts specializing in both the IC industry and academia, this carefully edited work uncovers new design opportunities in body area networks, medical implants, satellite communications, automobile radar detection, and wearable electronics. The book is divided into three sections: wireless system perspectives, chip architecture and implementation issues, and devices and technologies used to fabricate wireless integrated circuits. Contributors address key issues in the development of future silicon-based systems, such as scale of integration,

ultra-low power dissipation, and the integration of heterogeneous circuit design style and processes onto one substrate. Wireless sensor network systems are now being applied in critical applications in commerce, healthcare, and security. This reference, which contains 25 practical and scientifically rigorous articles, provides the knowledge communications engineers need to design innovative methodologies at the circuit and system level.

Efficiency Enhancement of Linear GaN RF power Amplifiers Using the Doherty Technique Springer

Building upon the success of the first edition (2007), *Wireless Transceiver Design 2nd Edition* is an accessible textbook that explains the concepts of wireless transceiver design in detail. The architectures and the detailed design of both traditional and advanced all-digital wireless transceivers are discussed in a thorough and systematic manner, while carefully watching out for clarity and simplicity. Many practical examples and solved problems at the end of each chapter allow students to thoroughly understand the mechanisms involved, to build confidence, and enable them to readily make correct and practical use of the applicable results and formulas. From the instructors' perspective, the book will enable the reader to build courses at different levels of depth, starting from the basic understanding, whilst allowing them to focus on particular elements of study. In addition to numerous fully-solved exercises, the authors include actual exemplary examination papers for instructors to use as a reference format for student evaluation. The new edition has been adapted with instructors/lecturers, graduate/undergraduate students and RF engineers in mind. Non-RF engineers

looking to acquire a basic understanding of the main related RF subjects will also find the book invaluable.

Mastering the Design of Modern Wireless Equipment and Systems Academic Press

The book offers unique insight into the modern world of wireless communication that included 5G generation, implementation in Internet of Things (IoT), and emerging biomedical applications. To meet different design requirements, gaining perspective on systems is important. Written by international experts in industry and academia, the intended audience is practicing engineers with some electronics background. It presents the latest research and practices in wireless communication, as industry prepares for the next evolution towards a trillion interconnected devices. The text further explains how modern RF wireless systems may handle such a large number of wireless devices. Covers modern wireless technologies (5G, IoT), and emerging biomedical applications. Discusses novel RF systems, CMOS low power circuit implementation, antennae arrays, circuits for medical imaging, and many other emerging technologies in wireless co-space. Written by a mixture of top industrial experts and key academic professors.

Highly Linear Integrated Wideband Amplifiers IGI Global

Advances in electronics have pushed mankind to create devices, ranging from - credible gadgets to medical equipment to spacecraft instruments. More than that, modern society is getting used to—if not dependent on—the comfort, solutions, and astonishing amount of information brought by these devices. One field that has continuously benefited from those advances is the radio frequency integrated circuit (RFIC)

design, which in its turn has promoted countless benefits to the mankind as a payback. Wireless communications is one prominent example of what the advances in electronics have enabled and their consequences to our daily life. How could anyone back in the eighties think of the possibilities opened by the wireless local area networks (WLANs) that can be found today in a host of places, such as public libraries, coffee shops, trains, to name just a few? How can a youngster, who lives this true WLAN experience nowadays, imagine a world without it? This book deals with the design of linear CMOS RF Power Amplifiers (PAs). The RF PA is a very important part of the RF transceiver, the device that enables wireless communications. Two important aspects that are key to keep the advances in RF PA design at an accelerate pace are treated: efficiency enhancement and frequency-tunable capability. For this purpose, the design of two different integrated circuits realized in a 0.11 μm technology is presented, each one addressing a different aspect. With respect to efficiency enhancement, the design of a dynamic supply RF power amplifier is treated, making up the material of Chaps. 2 to 4.

2018 Cambridge University Press

This book tackles both high efficiency and high linearity power amplifier (PA) design in low-voltage CMOS. With its emphasis on theory, design and implementation, the book offers a guide for those actively involved in the design of fully integrated CMOS wireless transceivers. Offering mathematical background, as well as intuitive insight, the book is essential reading for RF design engineers and researchers and is also suitable as a text book.

mm-Wave Silicon Power Amplifiers and Transmitters Springer Science & Business Media

Improving the performance of existing technologies has always been a focal practice in the development of computational systems. However, as circuitry is becoming more complex, conventional techniques are becoming outdated and new research methodologies are being implemented by designers. Performance Optimization Techniques in Analog, Mix-Signal, and Radio-Frequency Circuit Design features recent advances in the engineering of integrated systems with prominence placed on methods for maximizing the functionality of these systems. This book emphasizes prospective trends in the field and is an essential reference source for researchers, practitioners, engineers, and technology designers interested in emerging research and techniques in the performance optimization of different circuit designs.

[Doherty Power Amplifiers](#) Walter de Gruyter GmbH & Co KG

Next generation mobile communication systems require the use of linear RF power amplifier for higher data transmission rates. However, linear RF power amplifiers are inherently inefficient and usually require additional circuits or further system adjustments for better efficiency. This dissertation focuses on the development of new efficiency enhancement schemes for

linear RF power amplifiers. The multistage Doherty amplifier technique is proposed to improve the performance of linear RF power amplifiers operated in a low power level. This technique advances the original Doherty amplifier scheme by improving the efficiency at much lower power level. The proposed technique is supported by a new approach in device periphery calculation to reduce AM/AM distortion and a further improvement of linearity by the bias adaptation concept. The device periphery adjustment technique for efficiency enhancement of power amplifier integrated circuits is also proposed in this work. The concept is clearly explained together with its implementation on CMOS and SiGe RF power amplifier designs. Furthermore, linearity improvement technique using the cancellation of nonlinear terms is proposed for the CMOS power amplifier in combination with the efficiency enhancement technique. In addition to the efficiency enhancement of power amplifiers, a scalable large-signal MOSFET model using the modified BSIM3v3 approach is proposed. A new scalable substrate network model is developed to enhance the accuracy of the BSIM3v3 model in RF and microwave applications. The proposed model simplifies the modeling of substrate coupling effects in MOS transistor and provides great accuracy in both small-signal and large-signal performances.

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