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The Embedded System Interconnect
An Engineering Approach
Algorithms and Hardware Implementations
The Hardware Software Interface
Computer Organization and Design RISC-V Edition
Computer Arithmetic Algorithms
The Petersen Graph
Computer Arithmetic
From 8086 to Pentium Processors
Fault-Tolerant Systems
13th International CSI Computer Conference, CSICC 2008 Kish Island, Iran, March 9-11, 2008 Revised Selected Papers
Introduction to Assembly Language Programming
The Datacenter as a Computer
Computer Organisation & Architecture
From Microprocessors to Supercomputers
Dependable Computing Systems
Computer Arithmetic
Algorithms and Architectures
COMPUTER ORGANIZATION AND DESIGN
Reliability of Computer Systems and Networks
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Introduction to Parallel Processing
Multi-Core Cache Hierarchies
Architecture, Reconfiguration, and Modeling
The Biblical Solution of Parallel Ministry (Acts 6:1–7)
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Interconnection Networks
Basic Computer Architecture
Algorithms and Hardware Designs
Software Testing and Quality Assurance
Computer Architecture
A Quantitative Approach

Encyclopedia of Big Data Technologies
An Introduction to Quantum Computing
Arrays · Trees · Hypercubes

CARLA CALI

FPGA, ASIC and Embedded Systems
Elsevier
A superior primer on software testing and quality assurance, from integration to execution and automation This important new work fills the pressing need for a user-friendly text that aims to provide software engineers, software quality professionals, software developers, and students with the fundamental developments in testing theory and common testing practices. Software Testing and Quality Assurance: Theory and Practice equips readers with a solid understanding of: Practices that support the production of quality software Software testing techniques Life-cycle models for requirements, defects, test cases, and test results Process models for units, integration, system, and acceptance testing How to build test teams, including recruiting and retaining test engineers Quality Models, Capability Maturity Model, Testing Maturity Model, and Test Process Improvement Model Expertly balancing theory with practice, and complemented with an abundance of pedagogical tools, including test questions, examples, teaching suggestions, and chapter summaries, this book is a valuable, self-contained tool for professionals and an ideal introductory text for courses in software testing, quality assurance, and software engineering.

Your Brain Is a Time Machine: The Neuroscience and Physics of Time
Tata McGraw-Hill Education
A team of recognized experts leads the way to dependable computing systems With computers and networks pervading every aspect of daily life, there is an ever-growing demand for dependability. In this unique resource, researchers and organizations will find the tools needed to identify and engage state-of-the-art approaches used for the specification, design, and assessment of dependable computer systems. The first part of the book addresses models and paradigms of dependable computing, and the second part deals with enabling technologies and applications. Tough issues in creating dependable computing systems are also tackled, including: Verification techniques Model-based evaluation Adjudication and data fusion Robust communications primitives Fault tolerance Middleware Grid security Dependability in IBM mainframes Embedded software Real-time systems Each chapter of this contributed work has been authored by a recognized expert. This is an excellent textbook for graduate and advanced undergraduate students in electrical engineering, computer engineering, and computer science, as well as a must-have reference that will help engineers, programmers, and technologists develop systems that are secure and reliable.
Paradigms, Performance Issues, and Applications Oxford University Press, USA
Designed as an introductory text for the students of computer science, computer applications, electronics engineering and information technology for their first course on the organization and

architecture of computers, this accessible, student friendly text gives a clear and in-depth analysis of the basic principles underlying the subject. This self-contained text devotes one full chapter to the basics of digital logic. While the initial chapters describe in detail about computer organization, including CPU design, ALU design, memory design and I/O organization, the text also deals with Assembly Language Programming for Pentium using NASM assembler. What distinguishes the text is the special attention it pays to Cache and Virtual Memory organization, as well as to RISC architecture and the intricacies of pipelining. All these discussions are climaxed by an illuminating discussion on parallel computers which shows how processors are interconnected to create a variety of parallel computers. KEY FEATURES Self-contained presentation starting with data representation and ending with advanced parallel computer architecture. Systematic and logical organization of topics. Large number of worked-out examples and exercises. Contains basics of assembly language programming. Each chapter has learning objectives and a detailed summary to help students to quickly revise the material.

Algorithms and Hardware Designs Instructor's Manual for Computer for Arithmetic Springer Science & Business Media

The authors provide an introduction to quantum computing. Aimed at advanced undergraduate and beginning graduate students in these disciplines, this text is illustrated with diagrams and exercises.

Synthesis of Arithmetic Circuits Springer Science & Business Media

Introduction to Parallel Algorithms and Architectures: Arrays Trees Hypercubes provides an introduction to the expanding field of parallel algorithms and architectures. This book focuses on parallel computation involving the most popular network architectures, namely, arrays, trees, hypercubes, and some closely related networks. Organized into three chapters, this book begins with an overview of the simplest architectures of arrays and trees. This text then presents the structures and relationships between the dominant network architectures, as well as the most efficient parallel algorithms for a wide variety of problems. Other chapters focus on fundamental results and techniques and on rigorous analysis of algorithmic performance. This book discusses as well a hybrid of network architecture based on arrays and trees called the mesh of trees. The final chapter deals with the most important properties of hypercubes. This book is a valuable resource for readers with a general technical background.

The Embedded System Interconnect John Wiley & Sons

This textbook is designed for the first course in Computer Architecture, usually offered at the junior/senior (3rd, 4th year) level in electrical engineering, computer science or computer engineering departments. This course is required of all electrical engineering and computer science/computer engineering majors specializing in the design of computer systems. This text provides a comprehensive introduction to computer architecture, covering topic from design of simple microprocessors to techniques used in the most advanced supercomputers.

An Engineering Approach Oxford University Press

The new RISC-V Edition of Computer Organization and Design features the

RISC-V open source instruction set architecture, the first open source architecture designed to be used in modern computing environments such as cloud computing, mobile devices, and other embedded systems. With the post-PC era now upon us, Computer Organization and Design moves forward to explore this generational change with examples, exercises, and material highlighting the emergence of mobile computing and the Cloud. Updated content featuring tablet computers, Cloud infrastructure, and the x86 (cloud computing) and ARM (mobile computing devices) architectures is included. An online companion Web site provides advanced content for further study, appendices, glossary, references, and recommended reading. Features RISC-V, the first such architecture designed to be used in modern computing environments, such as cloud computing, mobile devices, and other embedded systems Includes relevant examples, exercises, and material highlighting the emergence of mobile computing and the cloud

Algorithms and Hardware Implementations Springer Science & Business Media

The merging of computer and communication technologies with consumer electronics has opened up new vistas for a wide variety of designs of computing systems for diverse application areas. This revised and updated third edition on Computer Organization and Design strives to make the students keep pace with the changes, both in technology and pedagogy in the fast growing discipline of computer science and engineering. The basic principles of how the intended behaviour of complex functions can be realized with the interconnected network of digital blocks are explained in an easy-to-understand style. WHAT IS NEW TO THIS EDITION : Includes a new chapter on Computer Networking, Internet, and Wireless Networks. Introduces topics such as wireless input-output devices, RAID technology built around disk arrays, USB, SCSI, etc. Key Features Provides a large number of design problems and their solutions in each chapter. Presents state-of-the-art memory technology which includes EEPROM and Flash Memory apart from Main Storage, Cache, Virtual Memory, Associative Memory, Magnetic Bubble, and Charged Couple Device. Shows how the basic data types and data structures are supported in hardware. Besides students, practising engineers should find reading this design-oriented text both useful and rewarding.

The Hardware Software Interface John Wiley & Sons

Ideal for graduate and senior undergraduate courses in computer arithmetic and advanced digital design, Computer Arithmetic: Algorithms and Hardware Designs, Second Edition, provides a balanced, comprehensive treatment of computer arithmetic. It covers topics in arithmetic unit design and circuit implementation that complement the architectural and algorithmic speedup techniques used in high-performance computer architecture and parallel processing. Using a unified and consistent framework, the text begins with number representation and proceeds through basic arithmetic operations, floating-point arithmetic, and function evaluation methods. Later chapters cover broad design and implementation topics-including techniques for high-throughput, low-power, fault-tolerant, and reconfigurable arithmetic. An appendix provides a

historical view of the field and speculates on its future.An indispensable resource for instruction, professional development, and research, Computer Arithmetic: Algorithms and Hardware Designs, Second Edition, combines broad coverage of the underlying theories of computer arithmetic with numerous examples of practical designs, worked-out examples, and a large collection of meaningful problems. This second edition includes a new chapter on reconfigurable arithmetic, in order to address the fact that arithmetic functions are increasingly being implemented on field-programmable gate arrays (FPGAs) and FPGA-like configurable devices. Updated and thoroughly revised, the book offers new and expanded coverage of saturating adders and multipliers, truncated multipliers, fused multiply-add units, overlapped quotient digit selection, bipartite and multipartite tables, reversible logic, dot notation, modular arithmetic, Montgomery modular reduction, division by constants, IEEE floating-point standard formats, and interval arithmetic.Readership: Graduate and senior undergraduate courses in computer arithmetic and advanced digital design.

Computer Organization and Design RISC-V Edition CRC Press

This original text provides comprehensive coverage of parallel algorithms and architectures, beginning with fundamental concepts and continuing through architectural variations and aspects of implementation. Unlike the authors of similar texts, Professor Parhami reviews the circuit model and problemdriven parallel machines, variants of mesh architectures, and composite and hierarchical systems, among other subjects. With its balanced treatment of theory and practical designs, classtested lecture material and problems, and helpful case studies, the book is suited to graduate and upperlevel undergraduate students of advanced architecture or parallel processing.

Computer Arithmetic Algorithms CRC Press

ÿThis textbook provides a perfect amalgam of the basics of computer architecture, intricacies of modern assembly languages and advanced concepts such as multiprocessor memory systems and I/O technologies. It shows the design of a processor from first principles including its instruction set, assembly-language specification, functional units, microprogrammed implementation and 5-stage pipeline. Computer Organisation and Architecture can serve as a textbook in both basic as well as advanced courses on computer architecture, systems programming, and microprocessor design. Additionally, it can also serve as a reference book for courses on digital electronics and communication. Salient Features: ? Balanced presentation of theoretical, qualitative and quantitative aspects of computer architecture ? Extensive coverage of the ARM and x86 assembly languages ? Extensive software support: Instruction set emulators, assembler, Logisim and VHDL design of the SimpleRisc processor

The Petersen Graph W. W. Norton & Company

The authors examine various areas of graph theory, using the prominent role of the Petersen graph as a unifying feature.

Computer Arithmetic OUP USA

This book is a comprehensive text on basic, undergraduate-level computer architecture. It starts from theoretical

preliminaries and simple Boolean algebra. After a quick discussion on logic gates, it describes three classes of assembly languages: a custom RISC ISA called SimpleRisc, ARM, and x86. In the next part, a processor is designed for the SimpleRisc ISA from scratch. This includes the combinational units, ALUs, processor, basic 5-stage pipeline, and a microcode-based design. The last part of the book discusses caches, virtual memory, parallel programming, multiprocessors, storage devices and modern I/O systems. The book's website has links to slides for each chapter and video lectures hosted on YouTube.

From 8086 to Pentium Processors John Wiley & Sons

The book provides comprehensive coverage of the fundamental concepts of computer organization and architecture. Its focus on real-world examples encourages students to understand how to apply essential organization and architecture concepts in the computing world. The book teaches you both the hardware and software aspects of the computer. It explains computer components and their functions, interconnection structures, bus structures, computer arithmetic, processor organization, memory organization, I/O functions, I/O structures, processing unit organization, addressing modes, instructions, instruction pipelining, instruction-level parallelism, and superscalar processors. The case studies included in the book help readers to relate the learned computer fundamentals with the real-world processors.

Fault-Tolerant Systems Springer Science & Business Media

Arabic Type-Making in the Machine Age is an in-depth historical study of the evolution of Arabic type under the influence of changing technologies in the twentieth century.

13th International CSI Computer Conference, CSICC 2008 Kish Island, Iran, March 9-11, 2008 Revised Selected Papers PHI Learning Pvt. Ltd.

The authoritative reference on the theory and design practice of computer arithmetic.

Introduction to Assembly Language Programming Tata McGraw-Hill Education

Fault-Tolerant Systems is the first book on fault tolerance design with a systems approach to both hardware and software. No other text on the market takes this approach, nor offers the comprehensive and up-to-date treatment that Koren and Krishna provide. This book incorporates case studies that highlight six different computer systems with fault-tolerance techniques implemented in their design. A complete ancillary package is available to lecturers, including online solutions manual for instructors and PowerPoint slides. Students, designers, and architects of high performance processors will value this comprehensive overview of the field. The first book on fault tolerance design with a systems approach Comprehensive coverage of both hardware and software fault tolerance, as well as information and time redundancy Incorporated case studies highlight six different computer systems with fault-tolerance techniques implemented in their design Available to lecturers is a complete ancillary package including online solutions manual for instructors and PowerPoint slides

The Datacenter as a Computer McGraw-Hill Education

This book describes warehouse-scale computers (WSCs), the computing platforms that power cloud computing

and all the great web services we use every day. It discusses how these new systems treat the datacenter itself as one massive computer designed at warehouse scale, with hardware and software working in concert to deliver good levels of internet service performance. The book details the architecture of WSCs and covers the main factors influencing their design, operation, and cost structure, and the characteristics of their software base. Each chapter contains multiple real-world examples, including detailed case studies and previously unpublished details of the infrastructure used to power Google's online services. Targeted at the architects and programmers of today's WSCs, this book provides a great foundation for those looking to innovate in this fascinating and important area, but the material will also be broadly interesting to those who just want to understand the infrastructure powering the internet. The third edition reflects four years of advancements since the previous edition and nearly doubles the number of pictures and figures. New topics range from additional workloads like video streaming, machine learning, and public cloud to specialized silicon accelerators, storage and network building blocks, and a revised discussion of data center power and cooling, and uptime. Further discussions of emerging trends and opportunities ensure that this revised edition will remain an essential resource for educators and professionals working on the next generation of WSCs.
Computer Organisation & Architecture Springer Science & Business Media

This textbook introduces readers to assembly and its role in computer programming and design. The author concentrates on covering the 8086 family of processors up to and including the Pentium. The focus is on providing students with a firm grasp of the main features of assembly programming, and how it can be used to improve a computer's performance. All of the main features are covered in depth: stacks, addressing modes, arithmetic, selection and iteration, as well as bit manipulation. Advanced topics include: string processing, macros, interrupts and input/output handling, and interfacing with such higher-level languages as C. The book is based on a successful course given by the author and includes numerous hands-on exercises.
From Microprocessors to Supercomputers Elsevier
With computers becoming embedded as controllers in everything fromnetwork servers to the routing of subway schedules to NASAmissions, there is a critical need to ensure that systems continueto function even when a component fails. In this book, bestsellingauthor Martin Shooman draws on his expertise in reliabilityengineering and software engineering to provide a complete andauthoritative look at fault tolerant computing. He clearly explainsall fundamentals, including how to use redundant elements in systemdesign to ensure the reliability of computer systems andnetworks. Market: Systems and Networking Engineers, Computer Programmers, ITProfessionals.